

Editor

Assembler

☐ 8085 Assembly Language Editor

Assembler

Disassembler

```
// 1's COMPLEMENT OF AN 8-BIT NUMBER
// The number to be complemented is stored in C050
// Answer is stored in C051
```

```
    LDA C050
    CMA
    STA C051
    HLT
```

```
// EXAMPLE -> C050=96
// Answer -> C051=49
```

```
# ORG C050
# DB 96H
```

Autocorrect

Assemble

Registers

Memory

Devices

☐ Registers :

Register	Value	7	6	5	4	3	2	1	0
Accumulator	69	0	1	1	0	1	0	0	1
Register B	00	0	0	0	0	0	0	0	0
Register C	00	0	0	0	0	0	0	0	0
Register D	00	0	0	0	0	0	0	0	0
Register E	00	0	0	0	0	0	0	0	0
Register H	00	0	0	0	0	0	0	0	0
Register L	00	0	0	0	0	0	0	0	0
Memory(M)	3A	0	0	1	1	1	0	1	0

Register	Value	S	Z	* AC	* P	* Cy
Flag Register	00	0	0	0	0	0

Type	Value
Stack Pointer(SP)	0000
Memory Pointer (HL)	0000
Program Status Word(PSW)	6900
Program Counter(PC)	0007
Clock Cycle Counter	40
Instruction Counter	5

SOD	SIO	INTR	TRAP	R7.5	R6.5	R5.5
0	0	0	0	0	0	0

For SIM instruction

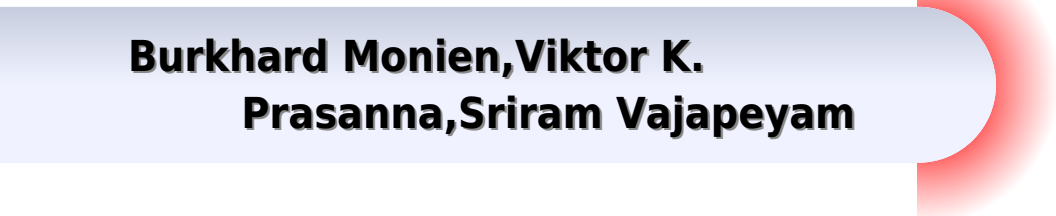
SOD	SOE	* R7.5	MSE	M...	M...	M...
0	0	0	0	0	0	0

For RIM instruction

SIO	I7.5	I6.5	I5.5	IE	M...	M...	M...
0	0	0	0	0	0	0	0

8085 Instruction Set Simulator

**Burkhard Monien, Viktor K.
Prasanna, Sriram Vajapeyam**



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