

lithography steppers increased from \$10 to \$35 million in this decade. Therefore, the cost of a modern factory varies between \$2 and \$3 billion. On the other hand, the cost per transistor decreases, because even though it is more expensive to build a circuit nowadays, more transistors are integrated onto one die.

Moreover, it is very likely that the design and verification costs are growing in the same proportion, impacting the final cost even more. For the 0.8 μm technology, the non-recurring engineering (NRE) costs were only about \$40,000. With each advance in IC technology, the NRE costs have dramatically increased. NRE costs for 0.18 μm design are around \$350,000, and at 0.13 μm , the costs are over \$1 million [20]. This trend is expected to continue at each subsequent technology node, making it more difficult for designers to justify producing an ASIC using nowadays technologies.

The time it takes for a design to be manufactured at a fabrication facility and returned to the designers in the form of an initial IC (turnaround time) is also increasing. Longer turnaround times lead to higher design costs, which may imply a loss of revenue if the design is late to the market.

Because of all these issues discussed before, there is a limit in the number of situations that can justify producing designs using the latest IC technology. Already in 2003, less than 1,000 out of every 10,000 ASIC designs had high enough volumes to justify fabrication at 0.13 μm [20]. Therefore, if design costs and times for producing a high-end IC are becoming increasingly large, just a few of them will justify their production in the future. The problems of increasing design costs and long turnaround times become even more noticeable due to increasing market pressures. The time available for a company to introduce a product into the market is shrinking. This way, the design of new ICs is increasingly being driven by time-to-market concerns.

Nevertheless, there will be a crossover point where, if the company needs a more customized silicon implementation, it will be necessary to afford the mask and production costs. However, economics are clearly pushing designers toward more regular structures that can be manufactured in larger quantities. Regular fabric would solve the mask cost and many other issues such as printability, extraction, power integrity, testing, and yield. Customization of a product, however, cannot rely solely on software programming, mostly for energy efficiency reasons. This way, some form of hardware adaptability must be present to ensure that low cost, mass produced devices can still be tuned for different applications needs, without redesign and fabrication costs.

1.5 Memory

Memories have been a concern since the early years of computing systems. Whether due to size, manufacturing cost, bandwidth, reliability or energy consumption, special care has always been taken when designing the memory structure of a system. The historical and ever growing gap between the access time of memories and the throughput of processors has also driven the development of very advanced

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