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Design and Modeling of Low Power VLSI Systems Sharma, Manoj, Gautam, Ruchi, Khan, Mohammad Ayoub, 2016-06-06 Very Large Scale Integration VLSI Systems refer to the latest development in computer microchips which are created by integrating hundreds of thousands of transistors into one chip Emerging research in this area has the potential to uncover further applications for VLSI technologies in addition to system advancements Design and Modeling of Low Power VLSI Systems analyzes various traditional and modern low power techniques for integrated circuit design in addition to the limiting factors of existing techniques and methods for optimization Through a research based discussion of the technicalities involved in the VLSI hardware development process cycle this book is a useful resource for researchers engineers and graduate level students in computer science and engineering

VLSI Design and Test Manoj Singh Gaur, Mark Zwolinski, Vijay Laxmi, D. Boolchandani, Virendra Sing, Adit Singh, 2013-12-13 This book constitutes the refereed proceedings of the 17th International Symposium on VLSI Design and Test VDAT 2013 held in Jaipur India in July 2013 The 44 papers presented were carefully reviewed and selected from 162 submissions The papers discuss the frontiers of design and test of VLSI components circuits and systems They are organized in topical sections on VLSI design testing and verification embedded systems emerging technology

Advances in VLSI, Communication, and Signal Processing Debashis Dutta, Haranath Kar, Chiranjeev Kumar, Vijaya Bhadauria, 2019-12-03 This book comprises select proceedings of the International Conference on VLSI Communication and Signal processing VCAS 2018 It looks at latest research findings in VLSI design and applications The book covers a wide range of topics in electronics and communication engineering especially in the area of microelectronics and VLSI design communication systems and networks and image and signal processing The contents of this book will be useful to researchers and professionals alike

FPGAs and Parallel Architectures for Aerospace Applications Fernanda Kastensmidt, Paolo Rech, 2015-12-07 This book introduces the concepts of soft errors in FPGAs as well as the motivation for using commercial off the shelf COTS FPGAs in mission critical and remote applications such as aerospace The authors describe the effects of radiation in FPGAs present a large set of soft error mitigation techniques that can be applied in these circuits as well as methods for qualifying these circuits under radiation Coverage includes radiation effects in FPGAs fault tolerant techniques for FPGAs use of COTS FPGAs in aerospace applications experimental data of FPGAs under radiation FPGA embedded processors under radiation and fault injection in FPGAs Since dedicated parallel processing architectures such as GPUs have become more desirable in aerospace applications due to high computational power GPU analysis under radiation is also discussed

Modelling of the Interaction of the Different Vehicles and Various Transport Modes Aleksander Ślădkowski, 2019-01-28 This book discusses various issues of modeling freight and passenger traffic and explores the common approaches and regional differences The latter may be a consequence of national legislation or the various approaches that are adopted by scientists around the globe It focuses on

the organization of transcontinental transport and aspects of planning and harmonizing the movement of various transport means particularly intermodal and multimodal transport New approaches to the prediction of transportation needs are also considered Written by international experts the book is divided into 2 parts the first part analyzes passenger transport while the second addresses freight transport It is intended wide audience including university professors graduate and Ph D students transport professionals and logistics specialist

Robust SRAM Designs and Analysis Jawar Singh, Saraju P. Mohanty, Dhiraj K. Pradhan, 2012-08-01 This book provides a guide to Static Random Access Memory SRAM bitcell design and analysis to meet the nano regime challenges for CMOS devices and emerging devices such as Tunnel FETs Since process variability is an ongoing challenge in large memory arrays this book highlights the most popular SRAM bitcell topologies benchmark circuits that mitigate variability along with exhaustive analysis Experimental simulation setups are also included which cover nano regime challenges such as process variation leakage and NBTI for SRAM design and analysis Emphasis is placed throughout the book on the various trade offs for achieving a best SRAM bitcell design Provides a complete and concise introduction to SRAM bitcell design and analysis Offers techniques to face nano regime challenges such as process variation leakage and NBTI for SRAM design and analysis Includes simulation set ups for extracting different design metrics for CMOS technology and emerging devices Emphasizes different trade offs for achieving the best possible SRAM bitcell design

Fast and Effective Embedded Systems Design Tim Wilmshurst, Rob Toulson, 2012-07-03 Fast and Effective Embedded Systems Design is a fast moving introduction to embedded system design applying the innovative ARM mbed and its web based development environment Each chapter introduces a major topic in embedded systems and proceeds as a series of practical experiments adopting a learning through doing strategy Minimal background knowledge is needed C C programming is applied with a step by step approach which allows the novice to get coding quickly Once the basics are covered the book progresses to some hot embedded issues intelligent instrumentation networked systems closed loop control and digital signal processing Written by two experts in the field this book reflects on the experimental results develops and matches theory to practice evaluates the strengths and weaknesses of the technology or technique introduced and considers applications and the wider context Numerous exercises and end of chapter questions are included A hands on introduction to the field of embedded systems with a focus on fast prototyping Key embedded system concepts covered through simple and effective experimentation Amazing breadth of coverage from simple digital i o to advanced networking and control Applies the most accessible tools available in the embedded world Supported by mbed and book web sites containing FAQs and all code examples Deep insights into ARM technology and aspects of microcontroller architecture Instructor support available including power point slides and solutions to questions and exercises

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monitor on which you are reading it now Such stuff is termed embedded systems and this book shows how to design and develop embedded systems at a professional level Because yes many people quietly make a successful career doing just that Building embedded systems can be both fun and intimidating Putting together an embedded system requires skill sets from multiple engineering disciplines from software and hardware in particular Building Embedded Systems is a book about helping you do things in the right way from the beginning of your first project Programmers who know software will learn what they need to know about hardware Engineers with hardware knowledge likewise will learn about the software side Whatever your background is Building Embedded Systems is the perfect book to fill in any knowledge gaps and get you started in a career programming for everyday devices Author Changyi Gu brings more than fifteen years of experience in working his way up the ladder in the field of embedded systems He brings knowledge of numerous approaches to embedded systems design including the System on Programmable Chips SOPC approach that is currently growing to dominate the field His knowledge and experience make Building Embedded Systems an excellent book for anyone wanting to enter the field or even just to do some embedded programming as a side project What You Will Learn Program embedded systems at the hardware level Learn current industry practices in firmware development Develop practical knowledge of embedded hardware options Create tight integration between software and hardware Practice a work flow leading to successful outcomes Build from transistor level to the system level Make sound choices between performance and cost Who This Book Is For Embedded system engineers and intermediate electronics enthusiasts who are seeking tighter integration between software and hardware Those who favor the System on a Programmable Chip SOPC approach will in particular benefit from this book Students in both Electrical Engineering and Computer Science can also benefit from this book and the real life industry practice it provides

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China Radio Frequency Identification Stefan Mangard, Patrick Schaumont, 2015-09-24 This book constitutes the thoroughly refereed post conference proceedings of the 11th International Workshop on Radio Frequency Identification held in New York USA in June 2015 The 10 revised full papers were carefully reviewed and selected from 23 submissions and are organized in topical sections on PUFs and applications side channels and countermeasures RFID system attacks and efficient implementations

FeFET Devices, Trends, Technology and Applications Balwinder Raj, Shiromani Balmukund Rahi, Nandakishor Yadav, 2025-06-04 FeFET Devices Trends Technology and Applications is essential for anyone seeking an in depth understanding of the latest advancements in ferroelectric devices as it offers comprehensive insights into research techniques novel materials and the historical context of semiconductor development This book serves as an encyclopedia of knowledge for state of the art research techniques for the miniaturization of ferroelectric devices This volume explores characteristics novel materials used modifications in device structure and advancements in model FET devices Though many devices following Moore's Law and More Moore are proposed a complete history of existing and proposed semiconductor devices is now available here This resource focuses on developments and research in emerging ferroelectric FET devices and their applications providing unique coverage of topics covering recent advancements and novel concepts in the field of miniaturized ferroelectric devices

Physics of Semiconductor Devices Simon M. Sze, Yiming Li, Kwok K. Ng, 2021-03-24 The new edition of the most detailed and comprehensive single volume reference on major semiconductor devices The Fourth Edition of Physics of Semiconductor Devices remains the standard reference work on the fundamental physics and operational characteristics of all major bipolar unipolar special microwave and optoelectronic devices This fully updated and expanded edition includes approximately 1 000 references to original research papers and review articles more than 650 high quality technical illustrations and over two dozen tables of material parameters Divided into five parts the text first provides a summary of semiconductor properties covering energy band carrier concentration and transport properties The second part surveys the basic building blocks of semiconductor devices including p n junctions metal semiconductor contacts and metal insulator semiconductor MIS capacitors Part III examines bipolar transistors MOSFETs MOS field effect transistors and other field effect transistors such as JFETs junction field effect transistors and MESFETs metal semiconductor field effect transistors Part IV focuses on negative resistance and power devices The book concludes with coverage of photonic devices and sensors including light emitting diodes LEDs solar cells and various photodetectors and semiconductor sensors This classic volume the standard textbook and reference in the field of semiconductor devices Provides the practical foundation necessary for understanding the devices currently in use and evaluating the performance and limitations of future devices Offers completely updated and revised information that reflects advances in device concepts performance and application Features discussions of topics of contemporary interest such as applications of photonic devices that convert optical energy to electric energy Includes numerous problem sets real world examples tables figures and

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Nanoelectronics, Circuits and Communication Systems Vijay Nath,J. K. Mandal,2020-04-01 This book features selected papers presented at the Fourth International Conference on Nanoelectronics Circuits and Communication Systems NCCS 2018 Covering topics such as MEMS and nanoelectronics wireless communications optical communications instrumentation signal processing the Internet of Things image processing bioengineering green energy hybrid vehicles environmental science weather forecasting cloud computing renewable energy RFID CMOS sensors actuators transducers telemetry systems embedded systems and sensor network applications in mines it offers a valuable resource for young scholars researchers and academics alike *Radiation Tolerant Electronics* Paul Leroux,2019-08-26 Research on radiation tolerant electronics has increased rapidly over the past few years resulting in many interesting approaches to modeling radiation effects and designing radiation hardened integrated circuits and embedded systems This research is strongly driven by the growing need for radiation hardened electronics for space applications high energy physics experiments such as those on the Large Hadron Collider at CERN and many terrestrial nuclear applications including nuclear energy and nuclear safety With the progressive scaling of integrated circuit technologies and the growing complexity of electronic systems their susceptibility to ionizing radiation has raised many exciting challenges which are expected to drive research in the coming decade In this book we highlight recent breakthroughs in the study of radiation effects in advanced semiconductor devices as well as in high performance analog mixed signal RF and digital integrated circuits We also focus on advances in embedded radiation hardening in both FPGA and microcontroller systems and apply radiation hardened embedded systems for cryptography and image processing targeting space applications *Innovative Applications of Nanowires for Circuit Design* Raj, Balwinder,2020-11-20 Nanowires are an important sector of circuit design whose applications in very large scale integration design VLSI have huge impacts for bringing revolutionary advancements in nanoscale devices circuits and systems due to improved electronic properties of the nanowires Nanowires are potential devices for VLSI circuits and system applications and are highly preferred in novel nanoscale devices due to their high mobility and high driving capacity Although the knowledge and resources for the fabrication of nanowires is currently limited it is predicted that with the advancement of technology conventional fabrication flow can be used for nanoscale devices specifically nanowires Innovative Applications of Nanowires for Circuit Design provides relevant theoretical frameworks that include device physics modeling circuit design and the latest developments in experimental fabrication in the field of nanotechnology The book covers advanced modeling concepts of nanowires along with their role as a key enabler for innovation in GLSI devices circuits and

systems While highlighting topics such as design simulation types and applications and performance analysis of nanowires this book is ideally intended for engineers practitioners stakeholders academicians researchers and students interested in electronics engineering nanoscience and nanotechnology **Electrical and Electronic Devices, Circuits and Materials** Suman Lata Tripathi, Parvej Ahmad Alvi, Umashankar Subramaniam, 2021-03-15 The increasing demand in home and industry for electronic devices has encouraged designers and researchers to investigate new devices and circuits using new materials that can perform several tasks efficiently with low IC integrated circuit area and low power consumption Furthermore the increasing demand for portable devices intensifies the search to design sensor elements an efficient storage cell and large capacity memory elements Electrical and Electronic Devices Circuits and Materials Design and Applications will assist the development of basic concepts and fundamentals behind devices circuits materials and systems This book will allow its readers to develop their understanding of new materials to improve device performance with even smaller dimensions and lower costs Additionally this book covers major challenges in MEMS micro electromechanical system based device and thin film fabrication and characterization including their applications in different fields such as sensors actuators and biomedical engineering Key Features Assists researchers working on devices and circuits to correlate their work with other requirements of advanced electronic systems Offers guidance for application oriented electrical and electronic device and circuit design for future energy efficient systems Encourages awareness of the international standards for electrical and electronic device and circuit design Organized into 23 chapters Electrical and Electronic Devices Circuits and Materials Design and Applications will create a foundation to generate new electrical and electronic devices and their applications It will be of vital significance for students and researchers seeking to establish the key parameters for future work

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BSIM-Bulk MOSFET Model for IC Design - Digital, Analog, RF and High-Voltage Chenming Hu, Harshit Agarwal, Chetan Gupta, Yogesh Singh Chauhan, 2023-04-26 BSIM Bulk MOSFET Model for IC Design Digital Analog RF and High Voltage provides in depth knowledge of the internal operation of the model The authors not only discuss the fundamental core of the model but also provide details of the recent developments and new real device effect models In addition the book covers the parameter extraction procedures addressing geometrical scaling temperatures and more There is also a dedicated chapter on extensive quality testing procedures and experimental results This book discusses every aspect of the model in detail and hence will be of significant use for the industry and academia Those working in the semiconductor industry often run into a variety of problems like model non convergence or non physical simulation results This is largely due to a limited understanding of the internal operations of the model as literature and technical manuals are insufficient This also creates huge difficulty in developing their own IP models Similarly circuit designers and researcher across the globe need to know

new features available to them so that the circuits can be more efficiently designed Reviews the latest advances in fabrication methods for metal chalcogenide based biosensors Discusses the parameters of biosensor devices to aid in materials selection Provides readers with a look at the chemical and physical properties of reactive metals noble metals transition metals chalcogenides and their connection to biosensor device performance **Proceedings of the 5th International Workshop on Reconfigurable Communication-centric Systems on Chip 2010 - ReCoSoC'10** Michael Hübner,2010

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